LEX System

2NOR02 Porting Guide

NVIDIA Orin Jetson Linux 36.4.3(JetPack6.2)

LEX SW Team 2025/5/12

History

Version	Date	Description	
1.0	2025/05/12	Create File	

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1. Overview





Jetson Orin Nano 8G	tegra234-p3768-0000+p3767-0003-nv.dtb
Jetson NX 8G	tegra234-p3768-0000+p3767-0001-nv.dtb
Jetson NX 16G	tegra234-p3768-0000+p3767-0000-nv.dtb

The NVIDIA Jetson Orin dev kit includes an EEPROM, whereas the 2NORO2 does not. Therefore, please refrain from installing the operating system via the SDK Manager. However, you may install DeepStream through the SDK Manager.

The following describes method for 2NOR02 on NVIDIA Jetson Orin OS

 How to Create 2NOR02 OS - without Compile Jetson Linux source code Jetson Linux 36.4.3 is part of JetPack 6.2, it includes Linux Kernel 5.15, an Ubuntu 22.04 based root file system



2.1. Download Driver Package (BSP) & Sample Root Filesystem (rootfs)

Download website : <u>https://developer.nvidia.com/embedded/jetson-linux-r3643</u>

leveloper.nvidia.com/embedded/jetso	n-linux-r3643
Vulkan S > Vulkan 1.3	Support on L4T
Downloa	Ids and Links
DPIVEPS	Driver Package (BSP)
DRIVERS	Sample Root Filesystem Second Download
	Jetson Linux API Reference
SOURCES	Driver Package (BSP) Sources
	Sample Root Filesystem Sources
DOCS	Jetson AGX Orin Developer Kit User Guide Jetson Orin Nano Developer Kit User Guide
	Release Notes
	Jetson Linux Developer Guide (online version)
	Software License Agreement
	Jetson Linux API Reference
	Release sha1sum hashes

(1) Uncompress Driver Package (BSP)

For example :



(2) Uncompress Sample Root Filesystem (rootfs)

For example:	_
# sudo tar -jxf Tegra_Linux_Sample-Root-Filesystem_R36.4.3_aarch64.tbz2 -C	
lex/Linux_for_Tegra/rootfs	
	_

ruunaan	/-Sys	stem-P	roduct			a∕ji		<mark>rldia-hdd/jinmy</mark> \$ sudo tar -jxf Tegra_Linux_Sample-Root-Filesystem_R36.4.3_aarch64.tbz2 -C lex/Linux_
egra/root	"s/							
.mmy@jimm	/-Sys	stem-P	roduct	-Name:/		a/ji		ridia-hdd/jimmy\$ cd lex/Linux_for_Tegra/rootfs/
Lmmy@jimm	/-Sys	stem-P	roduct	-Name:/		a/ji		ridia-hdd/jimmy/lex/Linux_for_Tegra/rootfs\$ ls -al
otal 84					_			
WXC-XC-X	18	root	root	4096	四	30	09:51	
wxrwxr-x	10	jimmy	jimmy	4096	_	8	09:56	
WXFWXFWX	1	root	root	7	Ξ.	18	2023	
WXC-XC-X		root	root	4096	四	18	2022	
WXF-XF-X		root	root	4096	Æ.,	18	2023	
WXC-XC-X	137	root	root	12288	+=		14:31	
wxr-xr-x		root	root	4096	四	18	2022	
WXFWXFWX		root	root		=	18	2023	
wxr-xr-x		root	root	4096	=	18	2023	
wxr-xr-x		root	root	4096	=	18	2023	
wxr-xr-x		root	root	4096	=	18	2023	
wxr-xr-x		root	root	4096	四	18	2022	
		jimmy	jimmy	62			09:42	README.txt
'WX		root	root	4096	Ξ.		2023	
wxr-xr-x	19	root	root	4096	+		2024	
WXFWXFWX		root	root		÷.	18	2023	
wxr-xr-x		root	root	4096	+=		2022	
wxr-xr-x		root	root	4096	=	18	2023	
wxr-xr-x		root	root	4096	四	18	2022	
wxrwxrwt		root	root	4096	+=		14:30	tmp
wxr-xr-x	11	root	root	4096	=	18	2023	
wxr-xr-x	14	root	root	4096		22	2023	

(3) Execute "apply_binaries.sh"



The basic setup of JP6.2 has been completed. Next, proceed with modifying the functions of the 2NOR02 carrier board.

- 2.2. Modify bootloader dts/dtsi file
- (1) Set the EEPROM size to 0

The NVIDIA Jetson Orin dev kit includes an EEPROM, whereas the 2NOR02 does not.

Modify "bootloader/generic/BCT/tegra234-mb2-bct-misc-p3767-0000.dts"



(2) Set the GPIO from input to output (depending on customer needs)

Modify "bootloader/tegra234-mb1-bct-gpio-p3767-hdmi-a03.dtsi"

Ť	<pre>gpio main default: default {</pre>
÷	gpio-input = <
÷	TEGRA234 MAIN GPIO(B, \odot)
÷	TEGRA234 MAIN GPIO(Y, 0)
÷	TEGRA234 MAIN GPIO(Y, 1)
÷	TEGRA234 MAIN GPIO(Y, 2)
÷	TEGRA234 MAIN GPIO(Y, 3)
÷	TEGRA234 MAIN_GPIO(Y, 4)
1	TEGRA234 MAIN_GPIO(Z, 1)
1	TEGRA234_MAIN_GPIO(Z, 3)
÷	TEGRA234_MAIN_GPIO(Z, 4)
1	TEGRA234_MAIN_GPIO(Z, 5)
÷	TEGRA234_MAIN_GPIO(Z, 6)
÷	TEGRA234_MAIN_GPIO(Z, 7)
÷	TEGRA234_MAIN_GPIO(P, 6)
÷	//TEGRA234_MAIN_GPIO(Q, 5)
÷	//TEGRA234_MAIN_GPIO(Q, 6)
÷	TEGRA234_MAIN_GPI0(R, 4)
÷	TEGRA234_MAIN_GPIO(R, 5)
÷	TEGRA234_MAIN_GPIO(M, 0)
÷	//TEGRA234_MAIN_GPIO(N, 1)
1	TEGRA234_MAIN_GPIO(G, 0)
1	//TEGRA234_MAIN_GPI0(G, 6)
1	TEGRA234_MAIN_GPIO(G, 7)
÷	//TEGRA234_MAIN_GPIO(H, 0)
÷	TEGRA234_MAIN_GPIO(H, 7)
÷	TEGRA234_MAIN_GPIO(I, 0)
÷	TEGRA234_MAIN_GPIO(I, 1)
÷	TEGRA234_MAIN_GPI0(I, 2)
÷	TEGRA234_MAIN_GPI0(AC, 6)
÷	TEGRA234_MAIN_GPIO(L, 2)
1	

		-
-	<pre>qpio-output-high = <</pre>	
į	TEGRA234 MAIN GPIO(Q, 3)	Ì
ł	TEGRA234_MAIN_GPIO(A, 0)	
ł	//lex++	
ł	<pre>TEGRA234_MAIN_GPIO(Q, 5)</pre>	ł
į.	<pre>TEGRA234_MAIN_GPIO(Q, 6)</pre>	Ì
ł	TEGRA234_MAIN_GPIO(N, 1)	ł
ł	TEGRA234_MAIN_GPIO(G, 6)	
ł	TEGRA234_MAIN_GPIO(H, 0)	
Ì	//lex++ end	
ł	>;	ł

Modify

"bootloader/generic/BCT/tegra234-mb1-bct-pinmux-p3767-hdmi-a03.dtsi"

```
soc_gpio32_pq5 {
              nvidia,pins = "soc_gpio32_pq5";
nvidia,function = "rsvd0";
              nvidia,pull = <TEGRA_PIN_PULL_UP>;
              nvidia,tristate = <TEGRA PIN ENABLE>;
              nvidia,enable-input = <TEGRA PIN ENABLE>;
              */
              nvidia,tristate = <TEGRA PIN DISABLE>;
              nvidia,enable-input = <TEGRA_PIN_DISABLE>;
nvidia,io-high-voltage = <TEGRA_PIN_DISABLE>;
nvidia,lpdr = <TEGRA_PIN_DISABLE>;
   };
   soc_gpio33_pq6 {
              nvidia,pins = "soc_gpio33_pq6";
nvidia,function = "rsvd0";
nvidia,pull = <TEGRA_PIN_PULL_UP>;
              nvidia,tristate = <TEGRA PIN ENABLE>;
              nvidia,enable-input = <TEGRA PIN ENABLE>;
              */
              nvidia,tristate = <TEGRA PIN DISABLE>;
              nvidia,enable-input = <TEGRA_PIN_DISABLE>;
              nvidia,io-high-voltage = <TEGRA_PIN_DISABLE>;
              nvidia,lpdr = <TEGRA PIN DISABLE>;
   };
!_
```

```
soc gpio39 pn1 {
          nvidia,pins = "soc gpio39 pn1";
         nvidia,function = "rsvdl";
          /*
         nvidia,pull = <TEGRA PIN PULL DOWN>;
          nvidia,tristate = <TEGRA PIN ENABLE>;
          nvidia,enable-input = <TEGRA PIN ENABLE>;
          nvidia,pull = <TEGRA PIN PULL UP>;
         nvidia,tristate = <TEGRA PIN DISABLE>;
          nvidia,enable-input = <TEGRA PIN DISABLE>;
         nvidia,lpdr = <TEGRA PIN DISABLE>;
 };
 soc gpio19 pg6 {
          nvidia,pins = "soc gpio19 pg6";
          nvidia,function = "rsvdl";
          /*
          nvidia,pull = <TEGRA PIN PULL DOWN>;
          nvidia,tristate = <TEGRA PIN ENABLE>;
          nvidia,enable-input = <TEGRA PIN ENABLE>;
          */
          nvidia,pull = <TEGRA PIN PULL UP>;
          nvidia,tristate = <TEGRA PIN DISABLE>;
          nvidia,enable-input = <TEGRA PIN DISABLE>;
          nvidia,lpdr = <TEGRA PIN DISABLE>;
 };
soc gpio21 ph0 {
         nvidia,pins = "soc gpio21 ph0";
        nvidia,function = "rsvd0";
         /*
        nvidia,pull = <TEGRA PIN PULL DOWN>;
        nvidia,tristate = <TEGRA PIN ENABLE>;
        nvidia,enable-input = <TEGRA PIN ENABLE>;
         */
        nvidia,pull = <TEGRA PIN PULL UP>;
        nvidia,tristate = <TEGRA PIN DISABLE>;
        nvidia,enable-input = <TEGRA PIN DISABLE>;
        nvidia,lpdr = <TEGRA PIN DISABLE>;
};
```

- 2.3. Flash Image to 2NOR02's NVME
- (1) Switch 2NOR02 into Force Recovery Mode

Connect your Linux host computer to the appropriate USB port on your 2NOR02 (USB typeC)

- 1. Ensure that the 2NOR02 is powered off.
- 2. Press and hold down the Force Recovery button (SW1).
- 3. 2NOR02 Power on.
- 4. Release the Force Recovery button (SW1)

To Determine Whether the 2NOR02 Is in Force Recovery Mode

Open a terminal window on your host computer and enter command" lsusb" . The Jetson module is in Force Recovery Mode if you see the message:

For example: Orin NX 16GB

Bus 001 Device 058: ID 0955:7423 NVIDIA Corp

(2) Flash Image to NVME

For example :

#sudo ./tools/kernel_flash/l4t_initrd_flash.sh --external-device nvme0n1p1 -c
tools/kernel_flash/flash_l4t_t234_nvme.xml -p " -c
bootloader/generic/cfg/flash_t234_qspi.xml" --showlogs --network usb0
p3509-a02-p3767-0000 internal

١.	
ł	junyjjimy-system-Product-Hune:/media/jimy/nvidia-hdd/jimy/lex/Linux_for_tegras\$ sude ./fools/kernel_flash/l4t_initrd_flash.shexternal-device numeOnips -c tools/kernel_flash/flash_l4t_t234_nume.xml .p * -c bootloader/generic/cfg/flash_t234_qspl.unitshoulogsnetwork usbb p3509-a02-p3707-0000 internal Please install the Secureboot package to use intuir of flash for jused board
ł.	e Entry added by WUDEA intrd flash tool mediajimmy/multia-hdd/inwy/multian.for_ingera/tools/kernel_flash/tmp 127.0.0.1(rw.nohlde.insecure.no_subtree_check_async.no_root_squash)
Ŀ	Export List for locaboat: //ord/a/list/vida-bod/list/mov/ex/list/arto-free/artoba/kernel_flash/tmp 127.0.0.1
ł.	[nedia/jinwy/nvidia-hdd/jinwy/lex/Linux_for_Tegra/tools/kernel_flash/l4t_initrd_flash_internal.shno-flashexternal-device nvme@njpi -c tools/kernel_flash/flash_l4t_t234_nvme.xml -p -c bootloader/ge ner/L/(fg/flash_t234_qspi.xmlshowlogsnetwork usb0 p3509-a02-p3707-0000 internal
ł.	* * Sten 1: Generate flack narkanse *
1	
1	Create folder to store images to flash
į.	Generate image for internal storage devices
i.	NUDITIONAL_DIB_OVERLAY=****/media/simy/nvidia-hdd/jinmy/lex/Linux_for_Tegra/fiash.shno-flashsignc bootloader/generic/cfg/flash_t234_eppi.anl p3509-a02-p3707-0000 internal
1	server
1	# R36 , REVISION: 4.3
i.	W User Felease: 0.0
1	ECID \s 0x80012344705DF35F5C00000015FD80C0
i.	<pre>copying device_config(/media/jikmy/nvidia-hdd/jikmy/tex/Linux_for_Tegra/booltoader/generic/BC1/tegra234-hbi-bct-device-p3767-0000.dts) done. copying his:config(/media/jikmy/nvidia-hdd/jikmy/tex/Linux_for_Tegra/booltoader/generic/BC1/tegra234-hbi-bct-his:config(/media/jikmy/nvidia-hdd/jikmy/nvidia-hd/jikmy/nvidia-hdd/jikmy/nvidia-hdd/jikmy/nvidia-hdd/jikmy/nvidia-hdd/jikmy/nvidia-hdd/jikmy/nvidia-hd/jikmy/nvidia-hd/jikmy/nvidia-hd/jikm Nvidia-hdvidia-hd/jikmy/nvidia-hdd/jikmy/nvidia-hdd/jikmy/nvidia-hd/jikmy/nvidia- Nvidia-hd/jikmy/nvidia-hd/jikmy/nvidia-hd/jikmy/nvidia-hd/jikmy/nvidia-hd/jikmy/nvidia</pre>
1	copying enc_fuse_dev_params(/mella/jinmy/nex/linux_for_fegra/boolloader/generic/BCT/tegra234-br-bct-diag-bool.dts) done.
1	Copying minfatchet_config(/hedia/)tmmy/hVtdi=hod)jtmmy/teX/Linux_for_iegra/bootloader/greerit/be/iftegra/24-hoi=bct-ratchet-p3/6/-0000.dt5) done. Existing encluse(/hedia/)timmy/hvtdi=hod/jtmmy/teX/Linux_for_iegra/bootloader/fuse_t234.xnl) reused.
i.	<pre>/tegraflash.py -chlp '0x23' - applet '/media/jinwy/nvidia-hdd/jinwy/kex/Linux_for_Tegra/bootLoader/hb1_t214_prod_bin' - skipuid -cfg readinfo_t214_min_prod_xmldev_params tegrafab-br-bc1-d126_pood_bin' - skipuid -cfg readinfo_t214_min_prod_xmldev_params tegrafab-bc1-bc1-bc1-bc1-bc1-bc1-bc1-bc1-bc1-bc</pre>
ί.	s
1	Nelcome to Tegra Flash version 1.0.0
1	Writing /mnt/internal/gspl_bootblob_ver.txt (109 bytes) into /dev/ntd0:66977792
i.	Copied 10% bytes from /mht/internai/dspi_bootblob_ver.txt to address oxosted000 in flash Writing gpt secondary 3 bin (partition: secondary gpt) into /dev/ntd0
i.	Shal checksum matched for /mnt/internal/gpt_secondary_3.0.bin
1	mrtting /mrt/nterna/ppt_secondary_5_bit (10590 bytes) into /dev/ntobio/svisob Copied 1650 bytes from /mrt/internal/ppt_secondary_3_bit in address 0x03/fbe00 in flash
i.	[225]: ldt_flash_from_kernel: Successfully flash_the_qspl [225]: ldt_flash_from_kernel: Slashfom_success
i.	225]: 14_ flash_from_kernel: The device size indicated in the partition layout xnl is smaller than the actual size. This utility will try to fix the GPT.
1	rias is successful Rebot device
1	Cleaning up
i.	ral rayare re curation industrianizer information

After the flashing process is complete, the NVIDIA Orin will reboot and display the Ubuntu installation screen.

2.4. Setup 2NOR02

After completing the Ubuntu installation, proceed with configuring the system settings

(1) Copy 2NOR02's dtb fils

copy files to /boot

Jetson Orin Nano 8G	tegra234-p3768-0000+p3767-0003-nv.dtb
Jetson NX 8G	tegra234-p3768-0000+p3767-0001-nv.dtb
Jetson NX 16G	tegra234-p3768-0000+p3767-0000-nv.dtb

	OVERLAYS , for SPI1/SPI3	tegra234-p3767-0000+p3509-a02-hdr40.dtbo
--	--------------------------	--

(2) Modify "/boot/extlinux/extlinux.conf"

Create new LABEL "lex" and set to default

DEFAULT lex LABEL lex MENU LABEL lex kernel LINUX /boot/Image INITRD /boot/initrd FDT /boot/tegra234-p3768-0000+p3767-0000-nv.dtb APPEND \${cbootargs} root=PARTUUID=a7be4080-ee56-482a-b639-1514ef9ea005 rw rootwait rootfstype=ext4 mminit_loglevel=4 console=ttyTCU0,115200 firmware_class.path=/etc/firmware fbcon=map:0 nospectre_bhb video=efifb:off console=tty0 OVERLAYS /boot/tegra234-p3767-0000+p3509-a02-hdr40.dtbo



(3) Reboot system

- 3. How to Create 2NOR02 OS Compile Jetson Linux source code
- Overview



	2NOR02
DP1_TX	HDMI
GBE_MDI	RJ45
PCIEO (X4)	NGFF1 (NVME)
PCIE2_0 (X2)	NGFF2 (M.2 Key B)
SPI1 1.8V	TPM (option)
UARTO	Pin Header (JP1), 3.3v
UART1	RS232(default),RS422(bon
	control)
UART2 (debug)	Pin Header, 3.3v

I2C0 3.3V	Type C and Wafer, 3.3v
I2C1 3.3V	Wafer ,3.3v
USBSSO + USB1	USB TypeA (CU2) 4port USB3
USBSS1 + USB0	USB TypeC (CU1)
USBSS2 + USB2	USB TypeA (CU3) 4port USB3
FAN PWM	Wafer
GPIO01	Pin Header (JP1)
GPIO07	Pin Header (JP1)
GPIO11	Pin Header (JP1)
GPIO12	Pin Header (JP1)
GPIO13	Pin Header (JP1)

3.1. Host computer Install and download necessary files

The OS version use by LEX is Ubuntu 22.04.6 LTS

(1) Install request packages
#sudo apt install wget lbzip2 build-essential bc zip libgmp-dev libmpfr-dev
libmpc-dev vim-common flex bison libssl-dev -y
#sudo apt-get install nfs-kernel-server zstd abootimg sshpass qemu-user-static sudo
apt-get install libxml2-utils -y

(2) Install GCC toolchain

https://developer.nvidia.com/embedded/jetson-linux-r3643 Download "Bootlin Toolchain gcc 11.3"

TOOLS	WebRTC
	Bootlin Toolchain gcc 11.3
	Bootlin Toolchain Sources, 2022.08-1

# mkdir \$HOME/l4t-gcc	
# cp -v aarch64glibcstable-2022.08-1.tar.bz2 \$HOME/l4t-gcc	
# cd \$HOME/l4t-gcc	
# tar xf aarch64glibcstable-2022.08-1.tar.bz2	
(3) Set environment	
# export	

CROSS_COMPILE=\$HOME/l4t-gcc/aarch64--glibc--stable-2022.08-1/bin/aarch64-buildroot-li nux-gnu-# export CROSS_COMPILE_AARCH64_PATH=\$HOME/l4t-gcc # export CROSS_COMPILE_AARCH64=\$HOME/l4t-gcc/aarch64--glibc--stable-2022.08-1/bin/aarch64buildroot-linux-gnu-# export LOCALVERSION=-tegra

3.2. Download Driver Package (BSP) and Sample Root Filesystem (rootfs) refer to "2.1. Download Driver Package(BSP) & Sample Root Filesystem (rootfs)"

3.3. Modify bootloader dts/dtsi file

refer to "2.2 Integrate modifications from the 2NOR02 into the bootloader"

- (1) Set the EEPROM size to 0
- (2) Set the GPIO from input to output (depending on customer needs)

3.4. Download Jetson-Linux JP6.2 source

Downlaod source	
cd \$HOME/Linux_for_Tegra/source	

Modify File "source_sync.sh"

-- DownloadAndSync "\$WHAT" "\${LDK_DIR}/\${WHAT}" "git://\${REPO}" "\${TAG}"
"\${OPT}"
++ DownloadAndSync "\$WHAT" "\${LDK_DIR}/\${WHAT}" "https://\${REPO}" "\${TAG}"
"\${OPT}"

Start download source

# ./source_sync.sh -k -t jetson	_36.4.3	

3.5. Enable USB3-2 and UARTO

Modify "tegra234-p3768-0000.dtsi"
gedit
\$HOME/Linux_for_Tegra/source/hardware/nvidia/t23x/nv-public/tegra234-p3768-0000.dts
i
<i>/</i> {
aliases {

```
//serial0 = &tcu;
         serial0 = "/bus@0/serial@3110000";
+
    };
     padctl@3520000 {
              status = "okay";
                   usb3 {
                        lanes {
                             //lex++
                             usb3-2 {
+
                                  nvidia,function = "xusb";
                                  status = "okay";
+
                             };
+
                        };
                   };
                   ports {
                        //lex++
                        usb3-2 {
+
                             nvidia,usb2-companion = <2>;
                             status = "okay";
                        };
+
                   };
    };
     usb@3610000 {
              status = "okay";
              phys = <&{/bus@0/padctl@3520000/pads/usb2/lanes/usb2-0}>,
                      <&{/bus@0/padctl@3520000/pads/usb2/lanes/usb2-1}>,
                      <&{/bus@0/padctl@3520000/pads/usb2/lanes/usb2-2}>,
                      <&{/bus@0/padctl@3520000/pads/usb3/lanes/usb3-0}>,
                      <&{/bus@0/padctl@3520000/pads/usb3/lanes/usb3-1}>,
                      <&{/bus@0/padctl@3520000/pads/usb3/lanes/usb3-2}>;
+
//lex++ usb3-2
              phy-names = "usb2-0", "usb2-1", "usb2-2", "usb3-0",
                        "usb3-1", "usb3-2"; //lex++ usb3-2
+
         };
```

- I
!

3.6. Enable TPM and UARTO

Modify

1_

"\$HOME/Linux_for_Tegra/source/hardware/nvidia/t23x/nv-public/nv-platform/tegra234 -p3768-0000+p3767-xxxx-nv-common.dtsi"

/{		
//	ex++	
+	serial@3	3110000 {
+	cor	mpatible = "nvidia.tegra194-hsuart";
+	res	set-names = "serial";
+	sta	tus = "okay";
+	};	
spi	@3230000	·{
	sta	tus = "okay";
//lex		
-	spi	@0 {
-		compatible = "tegra-spidev";
-		reg = <0x0>;
-		spi-max-frequency = <50000000>;
-		controller-data {
-		nvidia,enable-hw-based-cs;
-		nvidia,rx-clk-tap-delay = <0x10>;
-		nvidia,tx-clk-tap-delay = <0x0>;
-		};
-	};	
-	spi	@1 {
-		compatible = "tegra-spidev";
-		reg = <0x1>;
-		spi-max-frequency = <50000000>;
-		controller-data {
-		nvidia, enable-hw-based-cs;
-		nvidia,rx-clk-tap-delay = <0x10>;
-		nvidia,tx-clk-tap-delay = <0x0>;

..... }; }; _ //lex ++ nvidia,clock-always-on; + slb9670: slb9670@0 { + compatible = "infineon,slb9670"; + reg = <0x1>; + spi-max-frequency = <33000000>; + status = "okay"; + controller-data { + nvidia, enable-hw-based-cs; + nvidia,rx-clk-tap-delay = <0x10>; + }; + }; + }; }; _____

3.7. Compile Image

e	
#cd \$HOME/Linux_f	or_Tegra/source/
#./nvbuild.sh -o \$HC	DME/Linux_for_Tegra/images

3.8. Copy Image and dtb

#cd \$HOME/Linux_for_Tegra/source/	
<pre>#cp -v images/kernel/kernel-jammy-src/arch/arm64/boot/Image</pre>	
\$HOME/Linux_for_Tegra/kernel	

Copy dtb

1.00	
	<pre>#cp -v images/kernel-devicetree/generic-dts/dtbs/*.* \$HOME/Linux_for_Tegra/kernel/dtb/</pre>
	<pre>#sudo cp -v images/kernel-devicetree/generic-dts/dtbs/*.*</pre>
	\$HOME/Linux_for_Tegra/rootfs/boot
. –	

3.9. Flash Image to NVME

refer to "2.3. Flash Image to 2NOR02's NVME"

3.10. Setup 2NOR02

After completing the Ubuntu installation, proceed with configuring the system settings

(1). Modify "extlinux.conf"

refer to "2.4. Setup 2NOR02

(2) Modify "extlinux.conf"

(2). Reboot System